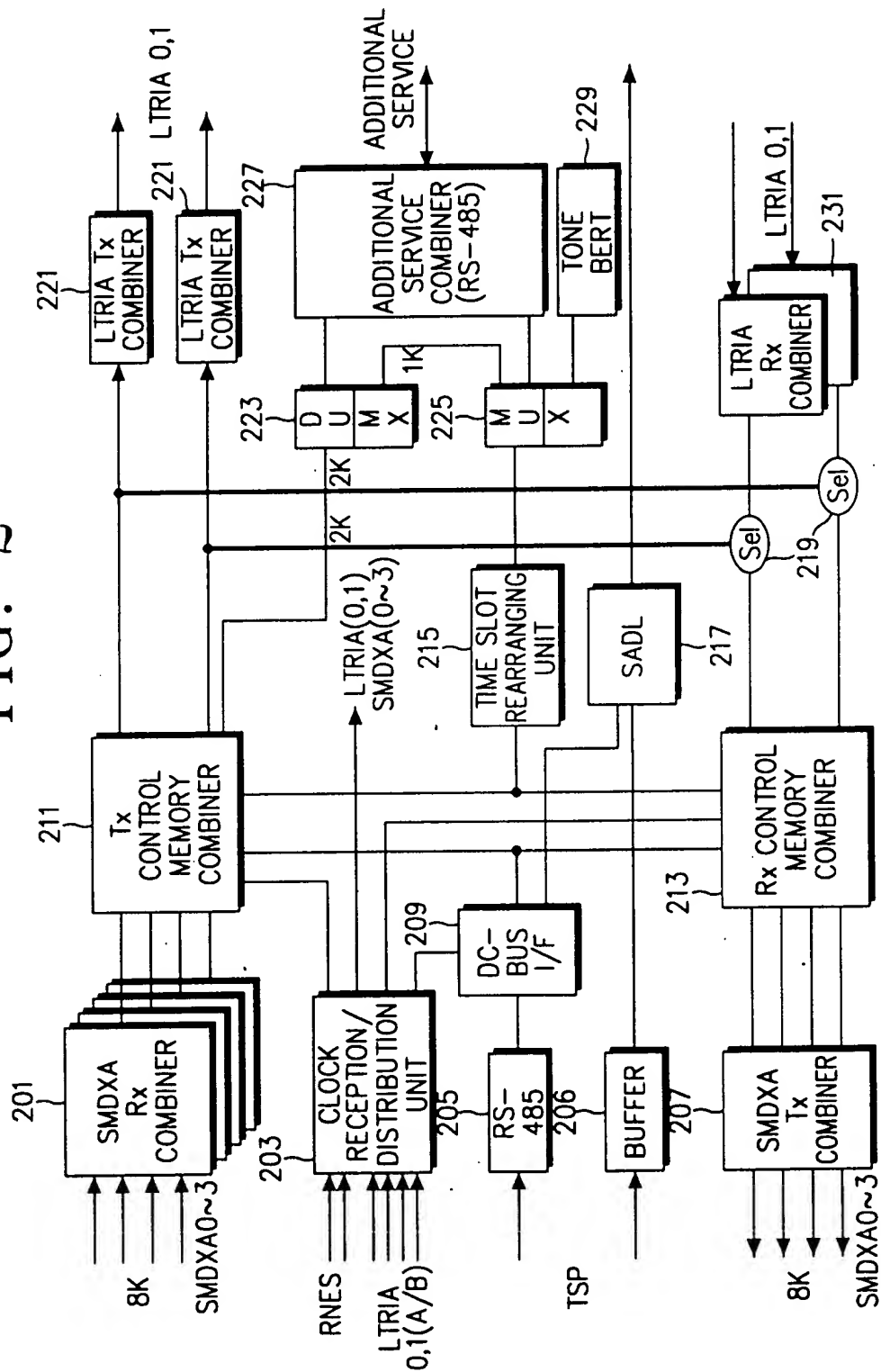


FIG. 1

FIG. 2



[illegible]

FIG. 3A

[CONTROL MEMORY ADDRESS]

MODE															4							
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0							
CMS2				CMS1				CMS0				OUTPUT CHANNEL				OUTPUT SHW				E/O		
0				CMSEL				2K TIME SLOT														

CMS2	CMS1	CMS0	CMSEL	CONTROL MEMORY OPERATION
0	0	0	0	TCM0
0	0	1	1	TCM1
0	1	0	2	TCM2
0	1	1	3	RCM0
1	0	0	4	RCM1
1	0	1	5	RCM2
1	1	0	6	RCM3
OUTPUT TS			2K-TIME SLOT OUTPUTTED TO SSL	
OUTPUT SHW			NUMBER OF 1K SUB-HIGHWAY OUTPUTTED TO INTRA-JUNCTOR	
OUTPUT CH			NUMBER OF 1K CHANNEL OUTPUTTED TO INTRA-JUNCTOR	
E/O			EVEN 1K/ODD 1K TIME SLOT,EVEN; 0, ADD; 1	

FIG. 3B

[CONTROL MEMORY DATA]

MODE		4													
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
LS	IB	CMS2CMS1CMS0			INPUT CH			INPUT SHW			E/O				
		SMSEL			INPUT TS										

SMS2	SMS1	SMS0	SMSEL	SPEECH MEMORY OPERATION	
				TCM(0~2)	RCM(0~3)
0	0	0	0	TSM0(n)	RSM0(n)
0	0	1	1	TSM1(n)	RSM1(n)
0	1	0	2	TSM2(n)	RSM2(n)
0	1	1	3	TSM3(n)	
1	1	0	6	TSMJ(n)	
1	0	0	4	LSMIJ0(2)	
1	0	1	5	LSMIJ1(2)	
INPUT TS				2K TIME-SLOT INPUTTED FROM SSL	
INPUT SHW				NUMBER OF 1K SUB-HIGHWAY INPUTTED FROM IJ OR FIJ	
INPUT CH				NUMBER OF 1K CHANNEL INPUTTED FROM IJ OR FIJ	
LS				INTER CALL LOOP 1:INTER, 0:LOOP	
IB				INSERTING AND RETRIEVING CUT-OFF-CUT-BITS BY TSCMA	

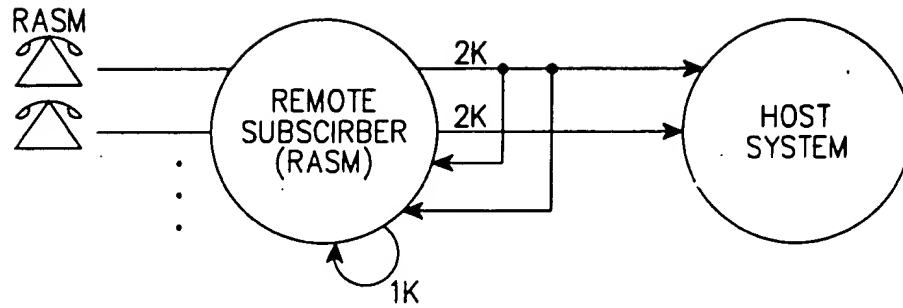


FIG. 4

009220" E46529150